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APPLICANTS

Jung-Chien Chang, Taoyuan Hsien, TAIWAN;

\*\* CONTINUING DATA \*\*\*\*\*  
 N/A JL

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 N/A JL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\* SMALL ENTITY \*\*  
 \*\* 10/03/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 3	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 1
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Verified and Acknowledged  
 Examiner's Signature \_\_\_\_\_ Initials \_\_\_\_\_

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TITLE  
 Method for preparing thin integrated circuits with multiple circuit layers

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